

A Layer-by-Layer Approach for Simulating Residual Stresses in AM

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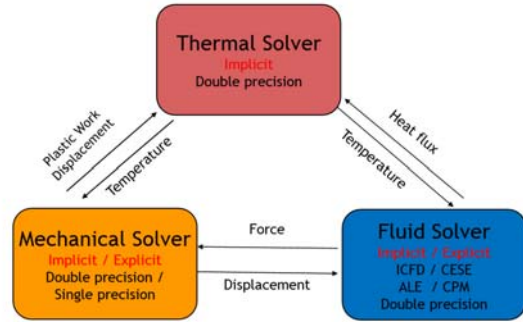
Outline

- Background & motivation
- Pre-processor *Pre4AM*
- *LOAD_HEAT_GENERATION
- *MAT_CWM_THERMAL
- *Tangent expansion coefficient
- *MAT_CWM
- Examples
- Summary

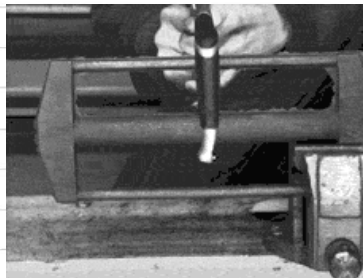
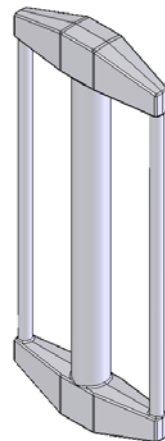
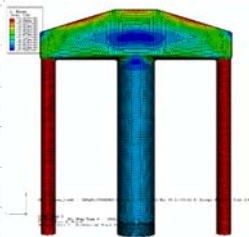


Aim & scope

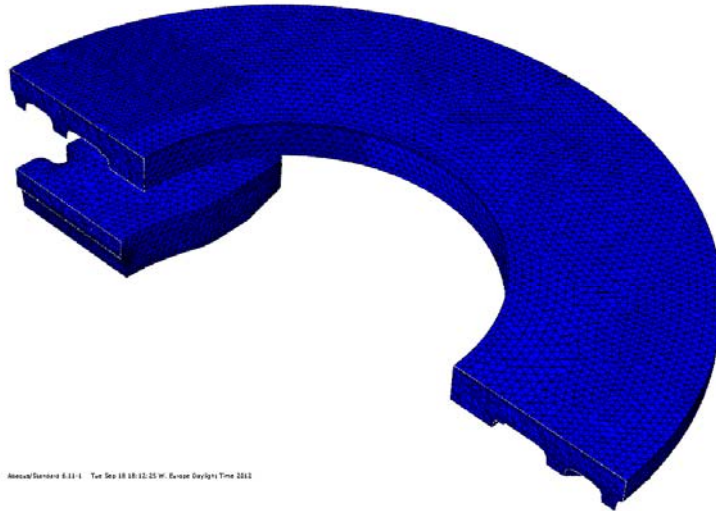
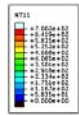
The aim of this project is to develop a rational approach for performing simulation of distortions and residual stresses developed in additive manufacturing (AM) of components using LS-Dyna. The approach must be easy to use in the design process and produces results in a reasonable computational time.



Residual stresses in castings



Residual stresses in brake discs



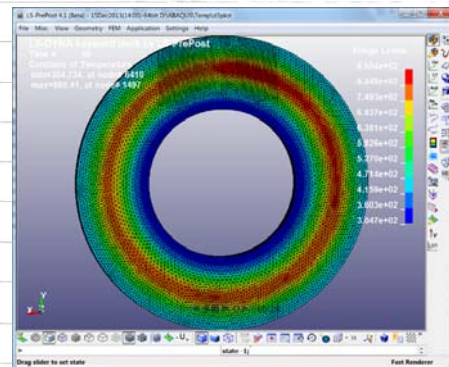
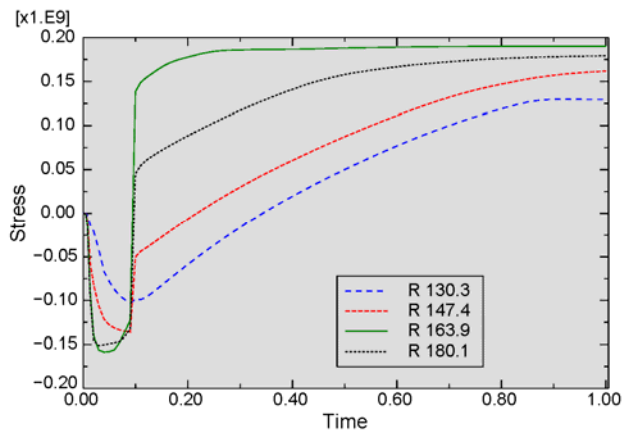
Step: Step-1 Frame: 0
Total Time: 0.000000

006: BrakeD_10.ans Ansys/Service 8.11.1 Tue Sep 18 10:12:25 W. Korea Daylight Time 2012

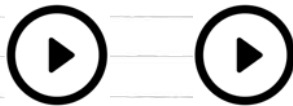
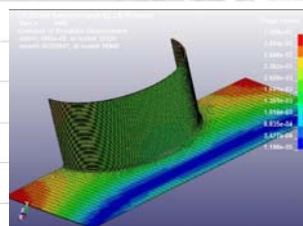
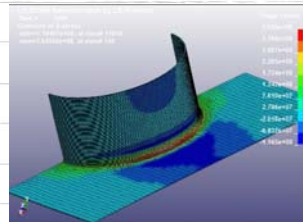
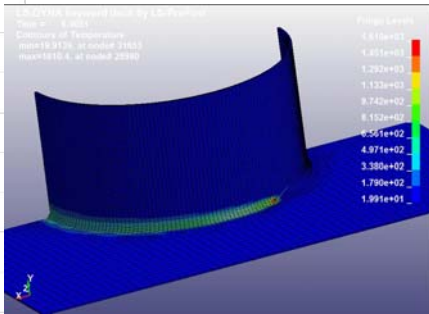


Step: Step-1
Element: 0 Step Time = 0.000
Primary Var: WT11

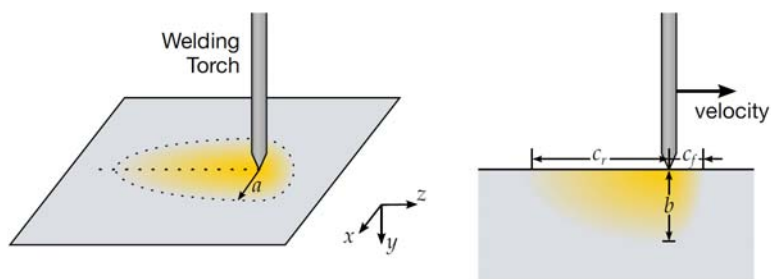
Residual stresses in brake discs



Residual stress in welding



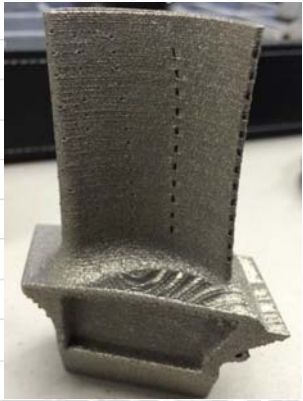
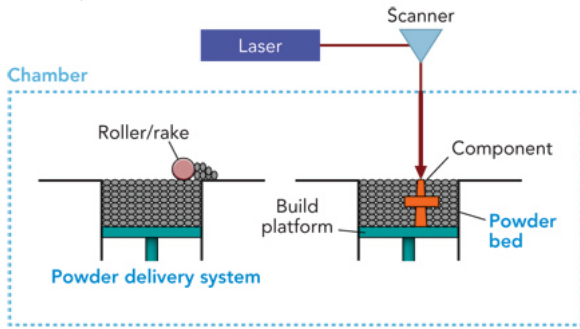
Goldak's heat source



$$q = \frac{6\sqrt{3}FQ}{\pi\sqrt{\pi abc}} \exp\left(\frac{-3x^2}{a^2}\right) \exp\left(\frac{-3y^2}{b^2}\right) \exp\left(\frac{-3z^2}{c^2}\right)$$

*BOUNDARY_THERMAL_WELD

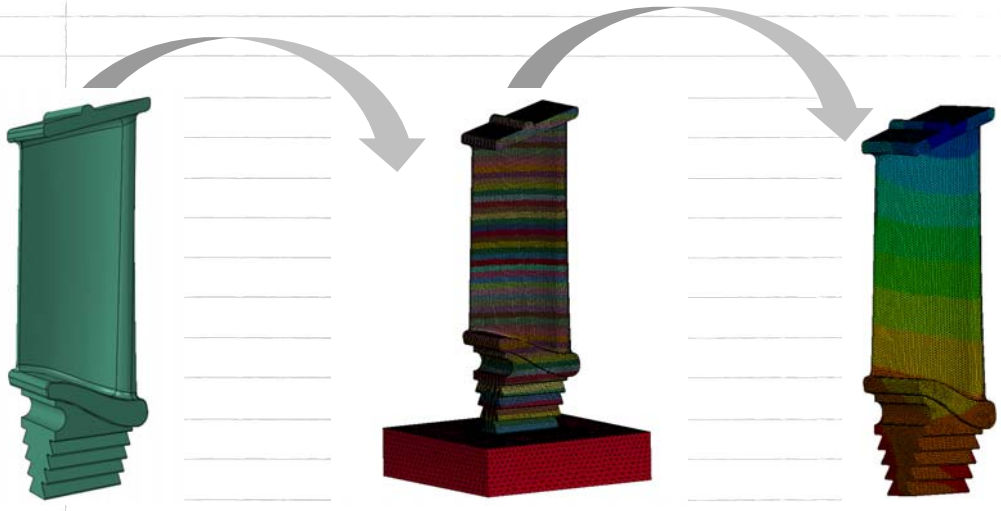
Additive manufacturing (AM)

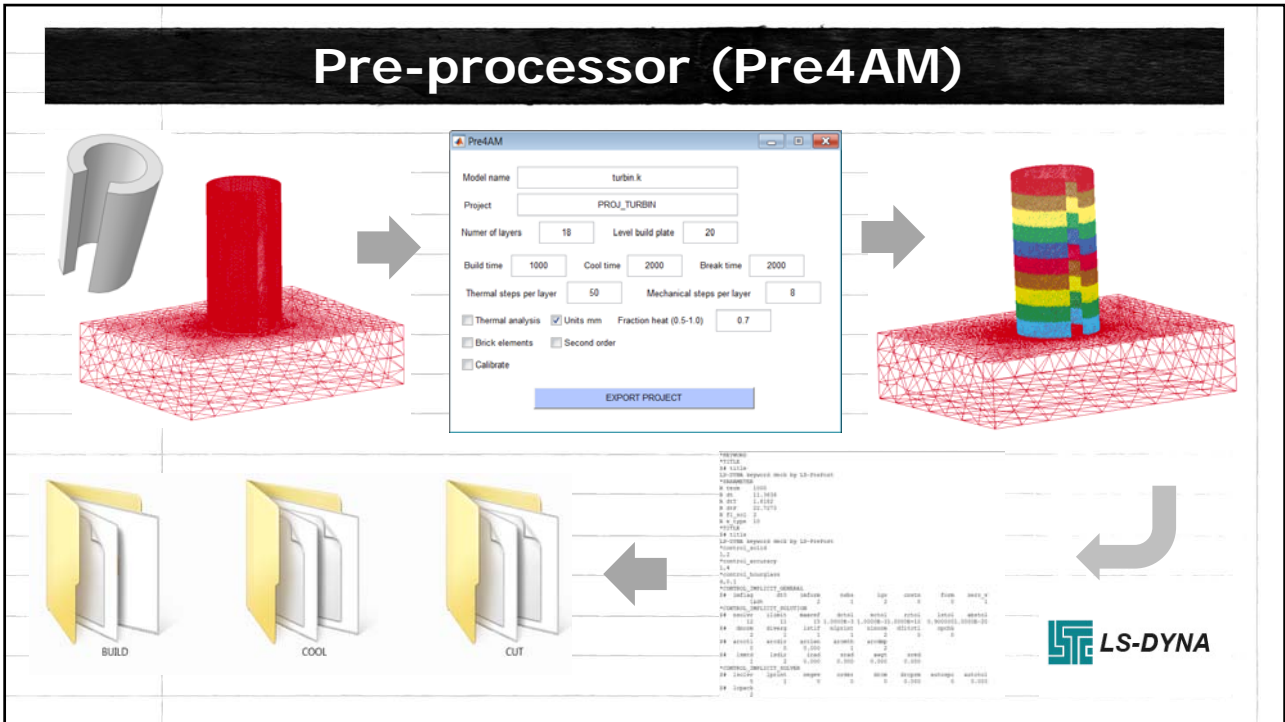


From <http://www.industrial-lasers.com>

From <http://www.additivemanufacturing.media>

Aim & scope





Pre-processor (Pre4AM)

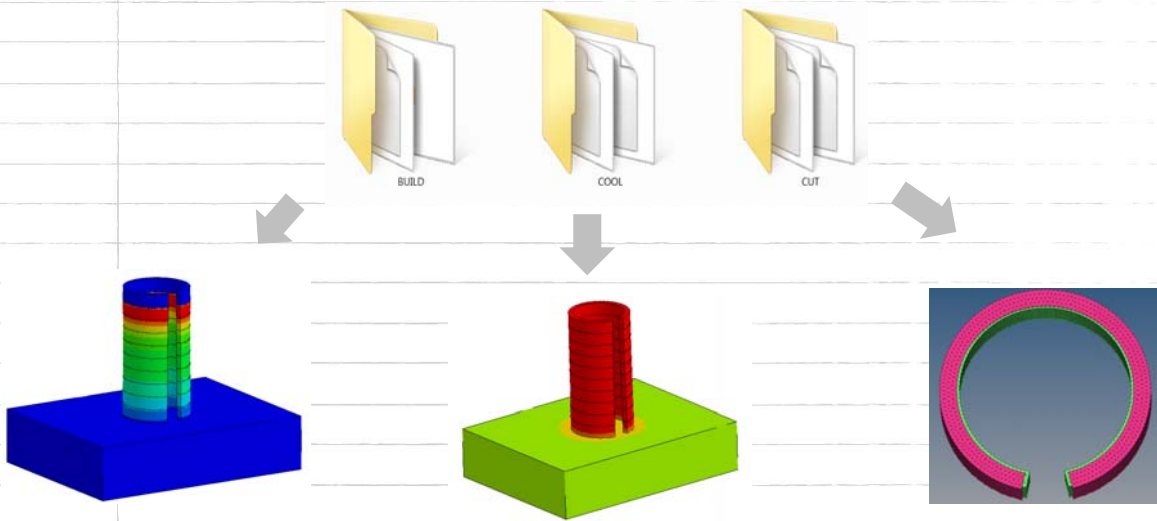
This GUI set up a LS-dyna simulation of residual stresses and distortions in AM.

A model is loaded and three analyses are created in the following maps: BUILD, COOL and CUT in the project map.

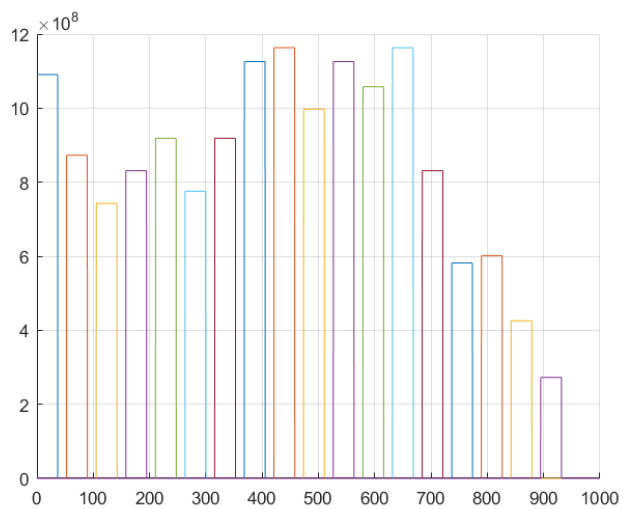
dynain_build, dynain_cool, temp_build.inc and temp_cool.inc are used for the restart analyses.

The following sets are needed: a node set for build plate temperature, a node set for cut displacement BC and two segments sets for convection BC, one for building and cooling, and the other one for cutting.

Pre-processor (Pre4AM)



*LOAD_HEAT_GENERATION



*MAT_CWM_THERMAL

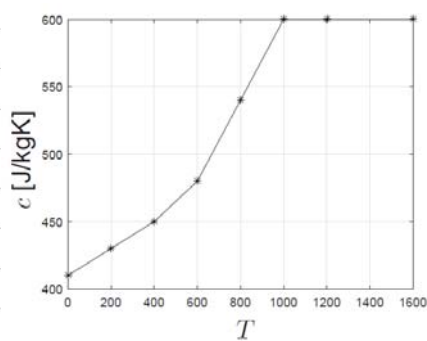
$$\rho c \dot{T} = k \Delta T + \boldsymbol{\sigma} : \dot{\boldsymbol{\epsilon}}^p + r$$

$$c(T) = c_m(T)\gamma + c_g(1 - \gamma)$$

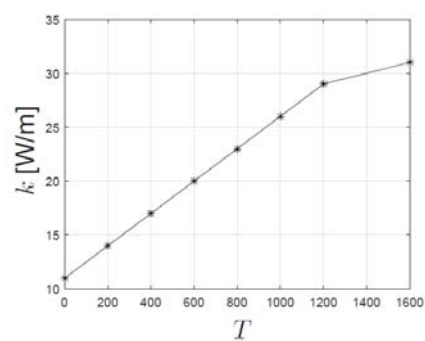
$$k(T) = k_m(T)\gamma + k_g(1 - \gamma)$$

$$\gamma = \min \left[1, \max \left(0, \frac{T^{\max} - T^{\text{start}}}{T^{\text{end}} - T^{\text{start}}} \right) \right]$$

Inconel 718



(a) Specific heat



(b) Conductivity

*MAT_CWM

$$\boldsymbol{\epsilon} = \boldsymbol{\epsilon}^e + \boldsymbol{\epsilon}^p + \boldsymbol{\epsilon}^t \quad \dot{\boldsymbol{\epsilon}}^t = \alpha(T)\dot{T}\mathbf{I}$$

$$E(T) = E_m(T)\gamma + E_g(1 - \gamma),$$

$$\nu(T) = \nu_m(T)\gamma + \nu_g(1 - \gamma),$$

$$\alpha = \alpha(T) = \alpha_m(T)\gamma + \alpha_g(1 - \gamma)$$

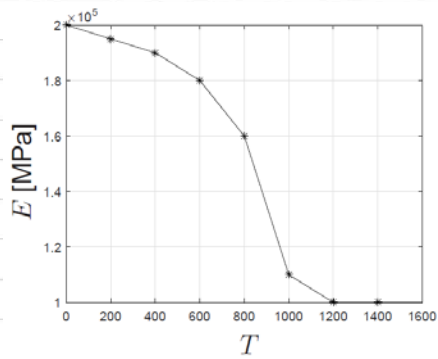
Tangent expansion coefficient

$$\alpha(T) = \frac{d\boldsymbol{\epsilon}^t}{dT} \quad \boldsymbol{\epsilon}^t = \hat{\alpha}(T)(T - T_0)\mathbf{I}$$

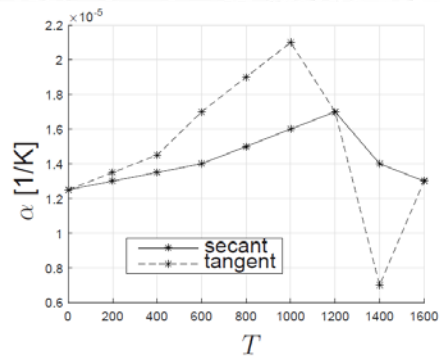
$$\dot{\boldsymbol{\epsilon}}^t = \frac{d\hat{\alpha}(T)}{dT}\dot{T}(T - T_0)\mathbf{I} + \hat{\alpha}(T)\dot{T}\mathbf{I}$$

$$\alpha(T) = \frac{d\hat{\alpha}(T)}{dT}(T - T_0)\mathbf{I} + \hat{\alpha}(T)\mathbf{I}$$

Inconel 718



(a) Young's modulus



(b) Thermal expansion

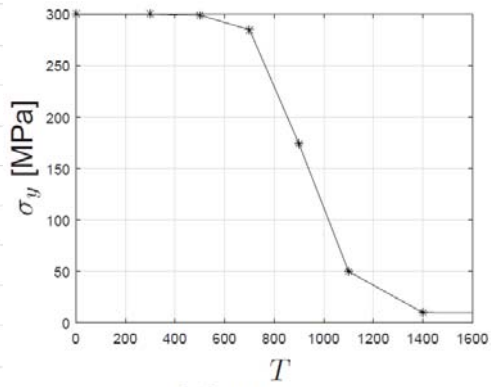
*MAT_CWM

$$f = \sqrt{\frac{3}{2}(s - \eta) : (s - \eta) - \sigma_y(T) - \beta H(T)\epsilon^p}$$

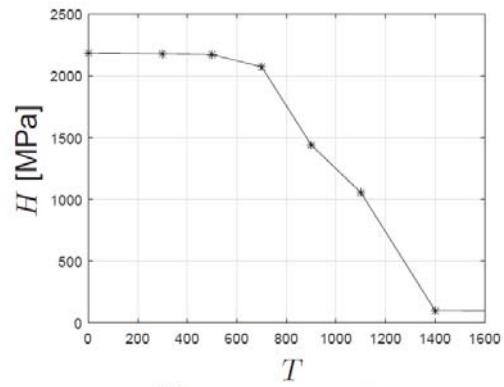
$$\epsilon_{n+1}^p = \epsilon_n^p \max \left[0, \min \left(1, \frac{T - T_a^e}{T_a^s - T_a^e} \right) \right],$$

$$\eta_{n+1}^p = \eta_n^p \max \left[0, \min \left(1, \frac{T - T_a^e}{T_a^s - T_a^e} \right) \right]$$

Inconel 718

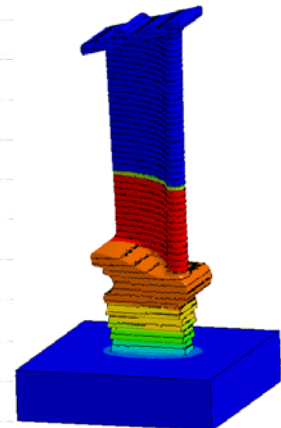
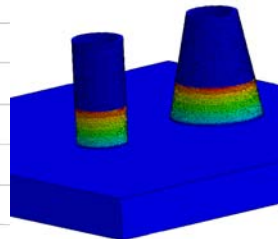
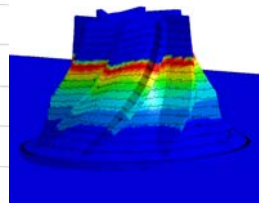
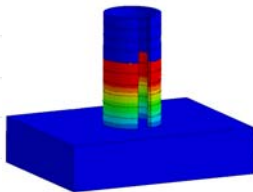
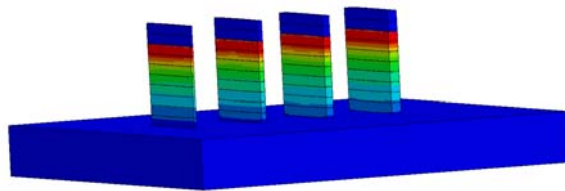
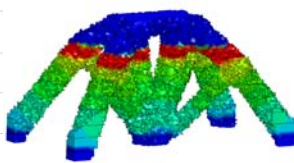


(a) Yield stress

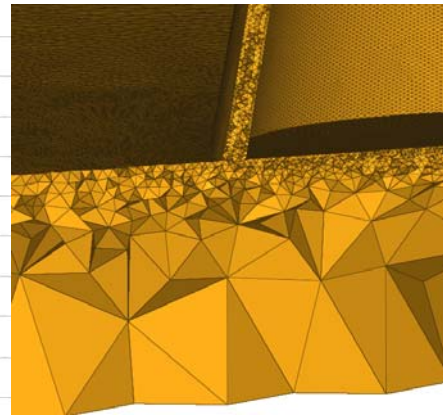
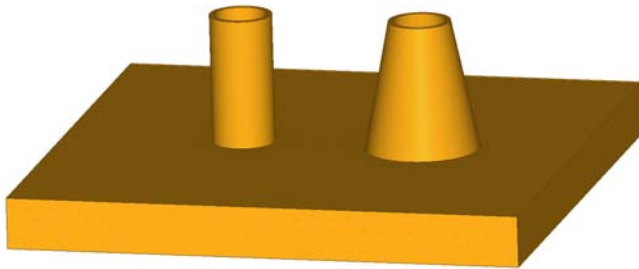


(b) Hardening modulus

Examples

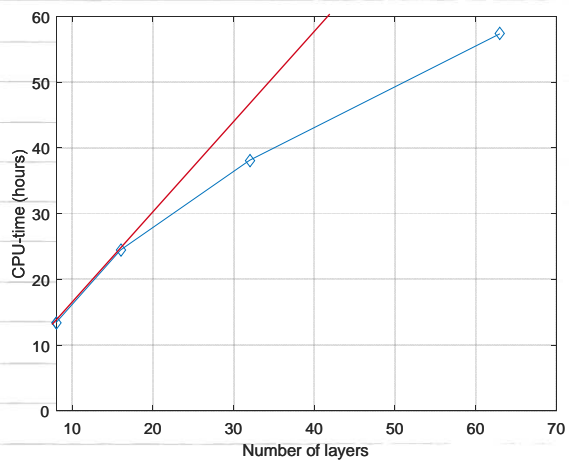
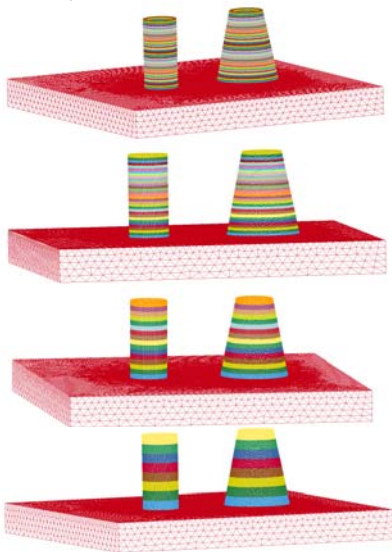


Benchmark – number of layers, CPU-time

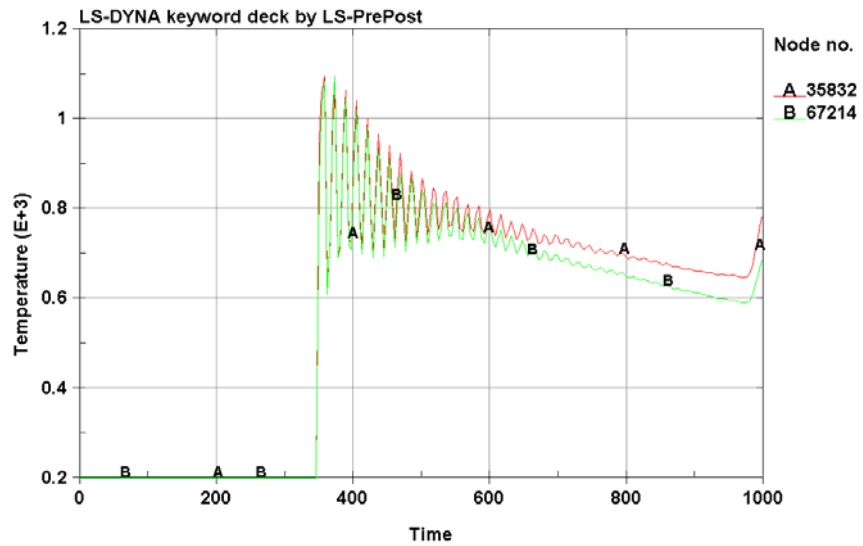


Benchmark of a cylinder and cone with more than 2 million elements.

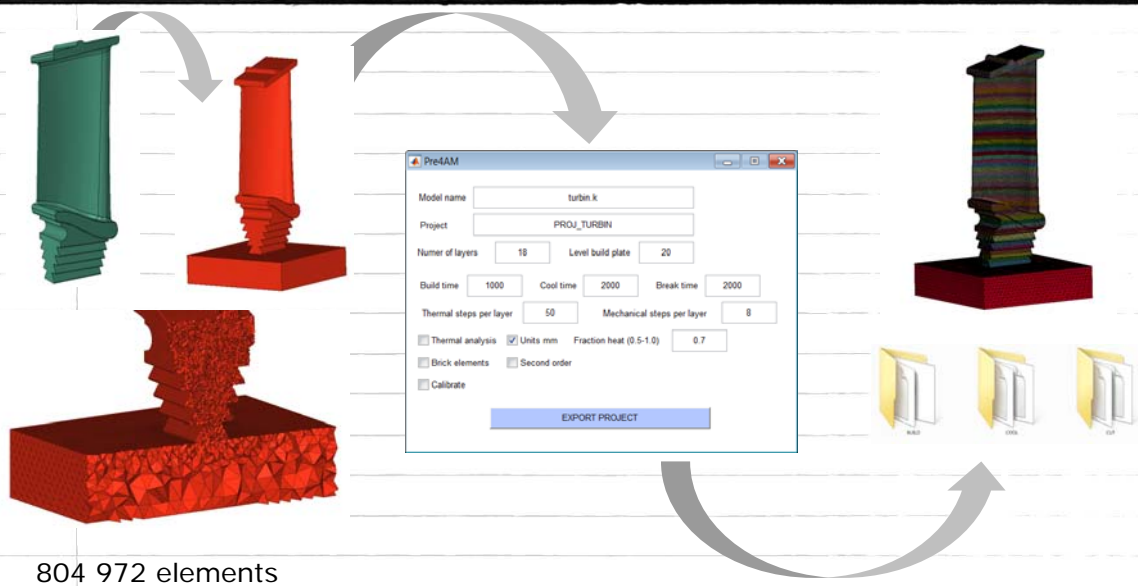
Benchmark – number of layers, CPU-time



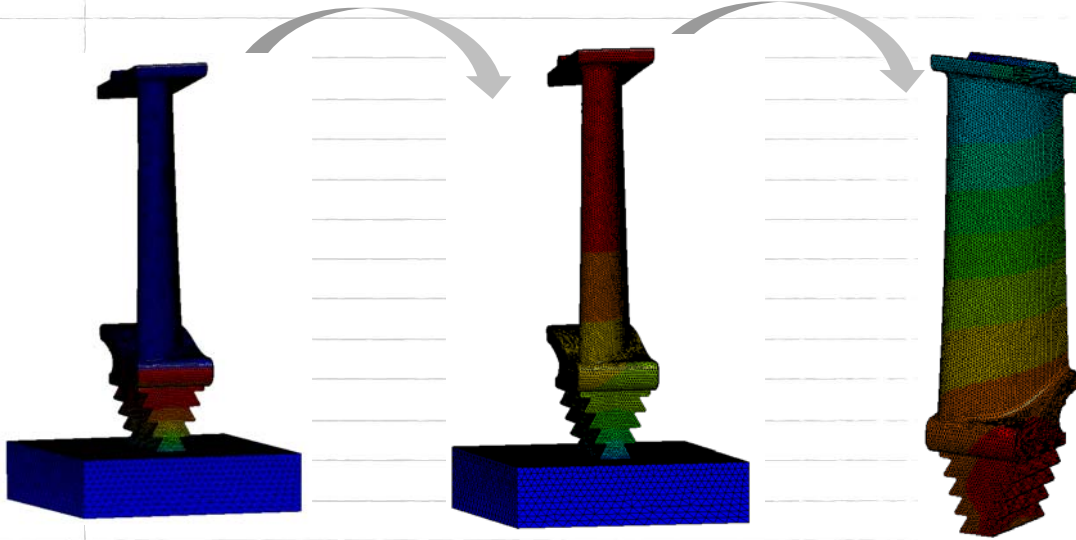
Benchmark – temperature history



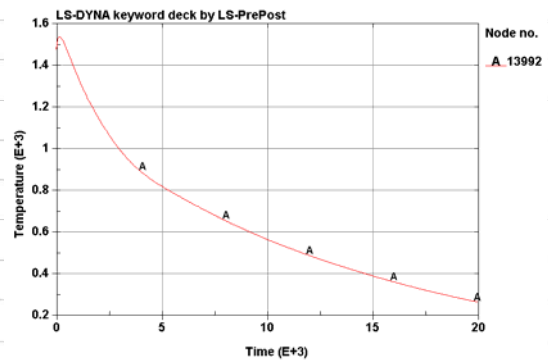
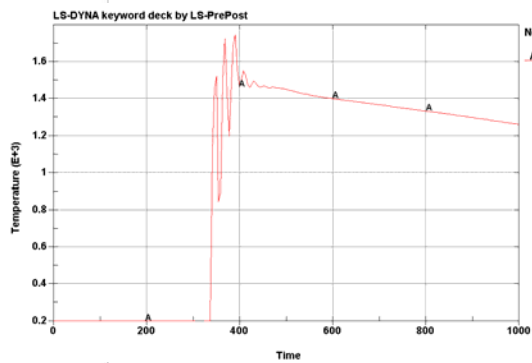
Example – Pre4AM



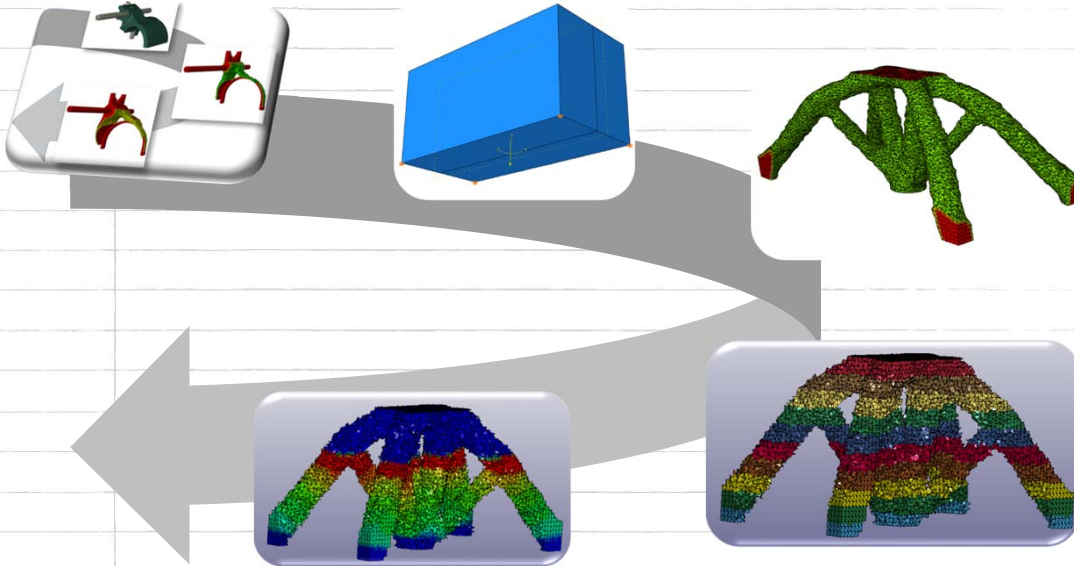
Example – build, cool, cut



Example – temperature history



CAE design for AM – www.digi3d.org



Summary

- ✓ A layer-by-layer approach for simulation of distortions and residual stresses developed in AM using *LOAD_HEAT_GENERATION, *MAT_CWM_THERMAL and *MAT_CWM is suggested.
- ✓ A first version of a pre-processor (Pre4AM) for setting up LS-Dyna simulations of proposed layer-by-layer approach is developed.
- ✓ Inconel 718 is implemented.
- ✓ Several examples are solved.
- ✓ The approach will be further developed within the project www.digi3d.org.

